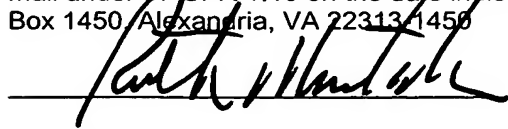


EXPRESS MAIL No.: EV 304 055 728 US

Deposited: January 30, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service Express mail under 39 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



/ Ruth Montalvo

Date: 01/30/04

Customer No. 026418

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Docket No. JG-SU-5196 / 500577.20055

Applicant(s): Kazuhiro HARADA, Yoji SUZUKI and Hidenobu ABE

Application No.:

Filed: concurrently herewith - January 30, 2004

For: METHOD OF MANUFACTURING SILICON SINGLE CRYSTAL AND  
SILICON SINGLE CRYSTAL MANUFACTURED BY THE METHOD

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

S I R:

Applicant herewith submits together with the simultaneously patent application this Information Disclosure Statement in accordance with the provisions of 37 C.F.R. §§ 1.97 and 1.98, and hereby make of record the following references:

	Document Number	Date	Name and/or Country	English Translation.
AA	6,045,610	04/04/2000	Park, et al.	
AL	11-1393	01/06/1999	JAPAN	English Abstract of Publication No. 2000-199048

Accompanying this Information Disclosure Statement and form PTO-1449 are copies of the AA and JP Publication of AL with English Abstract. Documents AA and AL are mentioned on page 3 of the specification.

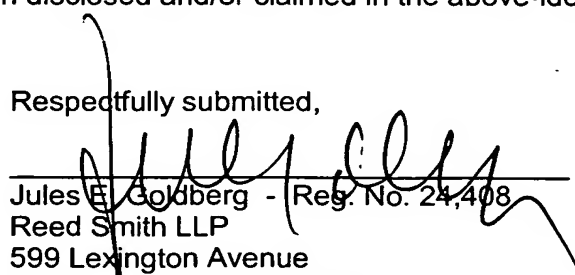
This submission is not an admission that the information disclosed in the documents is material to the patentability of the invention disclosed and/or claimed in the above-identified application.

Respectfully submitted,

01/30/04  
JEG:ram

Tel. (212) 521-5400

Enclosures:  
PTO-1449  
2 documents  
English Abstract



Jules E. Goldberg - Reg. No. 24,408  
Reed Smith LLP  
599 Lexington Avenue  
New York, NY 10022-7650

**LIST OF PRIOR ART CITED BY APPLICANT**  
(Filed on January 30, 2004)

Docket No. **JG-SU-5196 / 500577.20055**Applicant(s): **Kazuhiro HARADA, Yoji SUZUKI and Hidenobu ABE**

Application No.

Group:

Filed: **concurrently herewith –January 30, 2004**

Examiner:

**U.S. PATENT DOCUMENTS**

Exam. Init		Document Number	Date	Name	Class	Sub-Class	Filing Date Appropriate
	<b>AA</b>	<b>6,045,610</b>	<b>04/04/2000</b>	<b>Park, et al.</b>			
	<b>AB</b>						
	<b>AC</b>						
	<b>AD</b>						
	<b>AE</b>						
	<b>AF</b>						
	<b>AG</b>						
	<b>AH</b>						
	<b>AI</b>						
	<b>AJ</b>						
	<b>AK</b>						

**FOREIGN PATENT DOCUMENTS**

		Document Number	Date	Country	CLASS	Sub-Class	Translation YES NO
	<b>AL</b>	<b>11-1393</b>	<b>01/06/1999</b>	<b>JAPAN</b>			<b>Abstract only 2000-199048</b>
	<b>AM</b>						
	<b>AN</b>						
	<b>AO</b>						
	<b>AP</b>						
	<b>AQ</b>						
	<b>AR</b>						
	<b>AS</b>						

**OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	<b>AV</b>	
	<b>AW</b>	
	<b>AX</b>	
	<b>AY</b>	
	<b>AZ</b>	

Examiner:

Date:

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.